SDLS021A, D3517, MAY 1990-REVISED AUGUST 1991

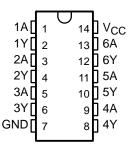
- Converts TTL-Voltage Levels to MOS Levels
- High Sink-Current Capability
- Input Clamping Diodes Simplify System Design
- Open-Collector Driver for Indicator Lamps and Relays
- Package Options Include "Small Outline" Packages, Ceramic Chip Carriers, and Standard and Ceramic 300-mil DIPs

description

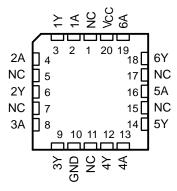
These monolithic hex buffers/drivers feature high-voltage open-collector outputs to interface with high-level circuits or for driving high-current loads. They are also characterized for use as buffers for driving TTL inputs. The 'LS07 has a rated output voltage of 30 V and the 'LS17 has a rated output voltage of 15 V. The maximum sink current is 30 mA for the SN54LS07 and 40 mA for the SN74LS07 and SN74LS17.

These circuits are compatible with most TTL families. Inputs are diode-clamped to minimize transmission-line effects, which simplifies design. Typical power dissipation is 140 mW and average propagation delay time is 12 ns.

SN54LS07 . . . J PACKAGE SN74LS07, SN74LS17 . . . D OR N PACKAGE (T0P VIEW)



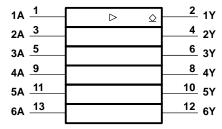
SN54LS07...FK PACKAGE (T0P VIEW)



NC - No internal connection

The SN54LS07 is characterized over the full military temperature range of –55°C to 125°C. The SN74LS07 and SN74LS17 are characterized for operation from 0°C to 70°C.

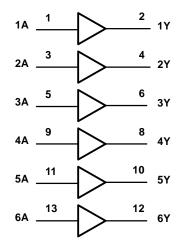
logic symbol†



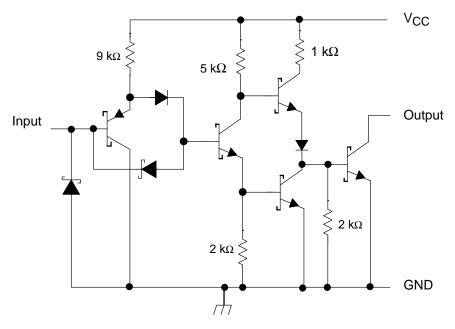
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

logic diagram (positive logic)



schematic (each gate)



Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC}		7 V
Input voltage, V _I (see Note 1)		5.5 V
Output voltage, VO (see Notes 1 and 2	2): SN54LS07, SN74LS07	30 V
-	SN74LS17	15 V
Operating free-air temperature range:	SN54LS07	–55°C to 125°C
	SN54LS07, SN74LS17 .	0°C to 70°C
Storage temperature range		65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

			SN54LS07			SN74LS07 SN74LS17			UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	
VCC	Supply voltage		4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage					2			V
VIL	Low-level input voltage				0.8			0.8	V
		'LS07			30			30	
۷он	High-level output voltage	'LS17						15	V
loL	Low-level output current	_			30			40	mA
TA	Operating free-air temperature		-55		125	0		70	°C



NOTES: 1. Voltage values are with respect to network ground terminal.

^{2.} This is the maximum voltage that should be applied to any output when it is in the off state.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS [†]		SN54LS07			SN74LS07 SN74LS17			UNIT	
				MIN	TYP [‡]	MAX	MIN	TYP [‡]	MAX	
VIK	$V_{CC} = MIN,$	I _I = -12 mA				-1.5			-1.5	V
I _{OH} V _{CC} = MIN,	V 2 V	'LS07, V _{OH} = 30 V			0.25			0.25	mA	
	ACC = IMILA'	$V_{IH} = 2 V$	'LS17, V _{OH} = 15 V			0.25			0.25	IIIA
\/ - ·	V _{CC} = MIN,	V _{IL} = 0.8 V	$I_{OL} = 16 \text{ mA}$			0.4			0.4	V
VOL			IOL = MAX§			0.7			0.7	V
lį	$V_{CC} = MAX$,	V _I = 7 V				1			1	mA
lН	$V_{CC} = MAX$,	V _I = 2.4 V				20			20	μΑ
Iμ	$V_{CC} = MAX$,	V _I = 0.4 V				-0.2			-0.2	mA
IССН	$V_{CC} = MAX$					14			14	mA
ICCL	$V_{CC} = MAX$					45			45	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (see Note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS		MIN	TYP	MAX	UNIT
t _{PLH}		V	D: 440.0	0: 45.5		6	10	
t _{PHL}	t _{PHL}	Y	$R_L = 110 \Omega, C_L = 15 pF$		19	30	ns	

NOTE 3: Load circuit and voltage waveforms are shown in Section 1 of TTL Logic Data Book, 1988.

 $[\]ddagger$ All typical values are at VCC = 5 V, TA = 25°C. § IOL = 30 mA for SN54 series parts and 40 mA for SN74 series parts.

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